Package Drawing

Designator: ES

Type: 12-Contact QFN, 3 mm × 3 mm, 0.75 mm thickness, with Fused Current Sense Loop

For Reference Only – Not for Tooling Use
(Reference DWG-0000222, Rev. 2 and DWG-0000382, Rev. 1)
Dimensions in millimeters
NOT TO SCALE
Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
Exact case and lead configuration at supplier discretion within limits shown

C PCB Layout Reference View

E Standard Branding Reference View

Lines 1, 2, 3 = 4 characters

Line 1: Part Number
Line 2: 4 digit Date Code
Line 3: Characters 5, 6, 7, 8 of Assembly Lot Number.

A Terminal #1 mark area.

B Fused sensed current path.

C Reference land pattern layout (reference IPC7351 QFN50P300X300X80-17W4M);
All pads a minimum of 0.20 mm from all adjacent pads; adjust as necessary to meet application process requirements and PCB layout tolerances; when mounting on a multilayer PCB, thermal vias at the exposed thermal pad land can improve thermal dissipation (reference EIA/JEDEC Standard JESD51-5).

D Coplanarity includes exposed current path and terminals.

E Branding scale and appearance at supplier discretion.

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