

Allegro MicroSystems UDN2916 Current Ratio/Timing Diagrams

	Full Step				Half Step				Modified Half Step											
Phase 1	1	1	0	0	1	1	1	X	0	0	0	X	1	1	1	X	0	0	0	X
Phase 2	1	0	0	1	1	X	0	0	0	X	1	1	1	X	0	0	0	X	1	1
I0_1	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	1	1	0	1	1
I1_1	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
I0_2	0	0	0	0	0	1	0	0	0	1	0	0	1	1	1	0	1	1	1	0
I1_2	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1	0	0

X = Irrelevant - No Current Flow in that Phase

