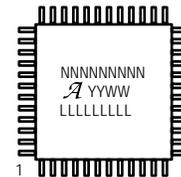
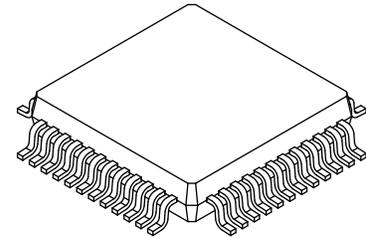


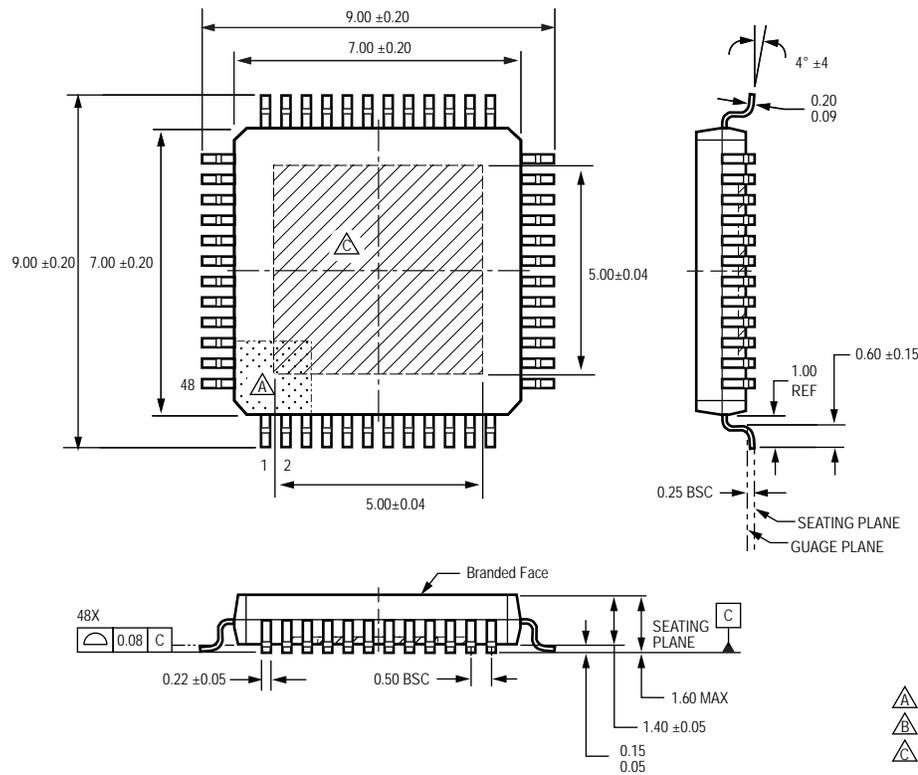
Designator: JP

Type: 48-pin LQFP with exposed thermal pad



 Standard Branding Reference View

N = Device part number
 A = Supplier emblem
 Y = Last two digits of year of manufacture
 W = Week of manufacture
 L = Lot number



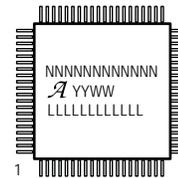
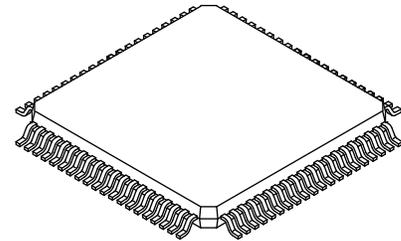
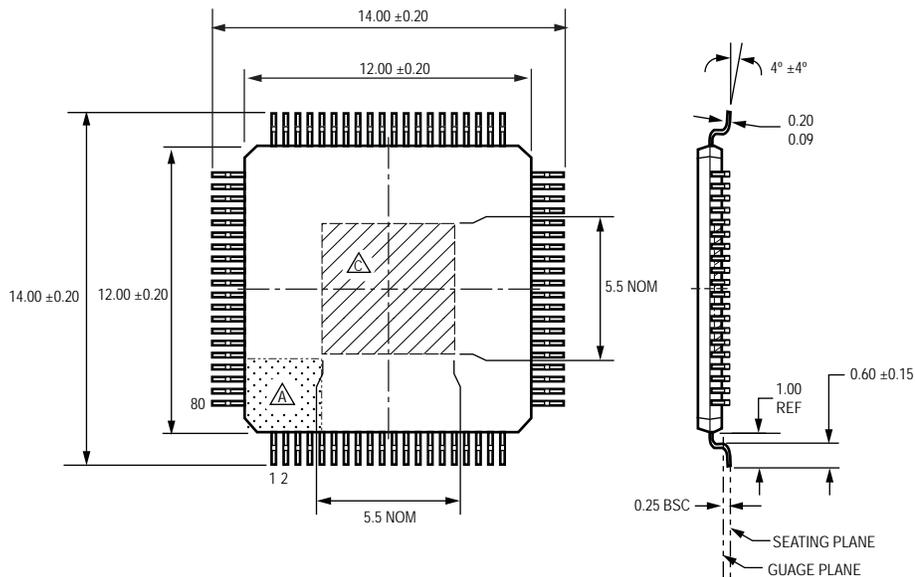
For Reference Only; not for tooling use (reference MS-026 BBCHD)
 Dimensions in millimeters
 Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
 Exact case and lead configuration at supplier discretion within limits shown

-  Terminal #1 mark area
-  Branding scale and appearance at supplier discretion
-  Exposed thermal pad (bottom surface); exact dimensions may vary with device

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Designator: JS

Type: 80-pin TQFP with exposed thermal pad

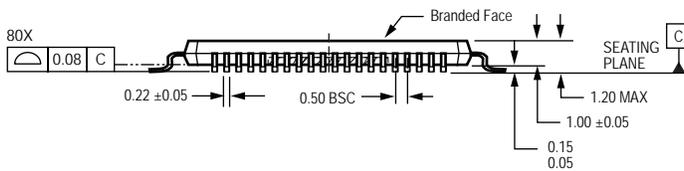


 Standard Branding Reference View

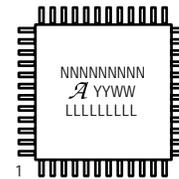
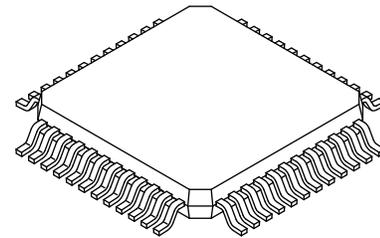
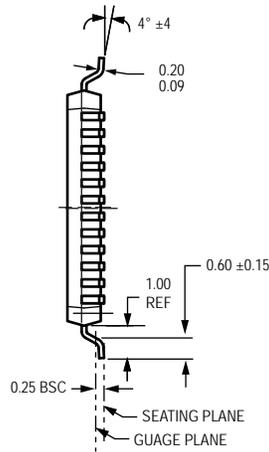
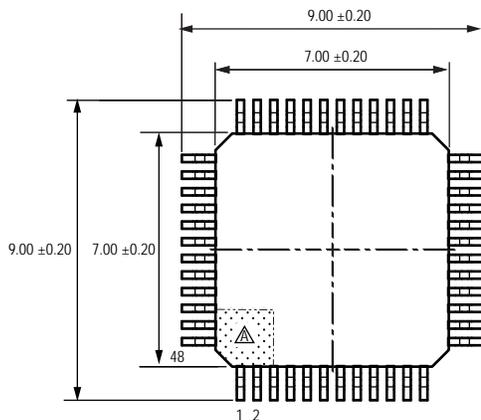
N = Device part number
 A = Supplier emblem
 Y = Last two digits of year of manufacture
 W = Week of manufacture
 L = Lot number

For Reference Only; not for tooling use (reference MS-026 ADDHD)
 Dimensions in millimeters
 Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
 Exact case and lead configuration at supplier discretion within limits shown

-  Terminal #1 mark area
-  Branding scale and appearance at supplier discretion
-  Exposed thermal pad (bottom surface); pad dimensions may vary with device

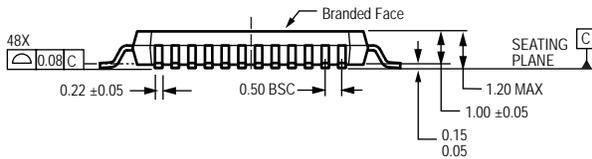


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 Standard Branding Reference View

N = Device part number
 A = Supplier emblem
 Y = Last two digits of year of manufacture
 W = Week of manufacture
 L = Lot number



For Reference Only; not for tooling use (reference MS-026 ABC)
 Dimensions in millimeters
 Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
 Exact case and lead configuration at supplier discretion within limits shown

-  Terminal #1 mark area
-  Branding scale and appearance at supplier discretion