

ASEK722, Board, Demo

85-0593-002-FAB  
Originator: S. Upton

Rev 3  
2/3/2014

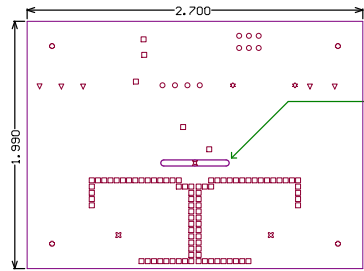
Layer Stack Up Detail for: 85-0593-002-R3.PCBD0C

Layer Name	Gerber Document	Copper Thickness
TopLayer	(.GTL)	5.6mil
BottomLayer	(.GBL)	5.6mil

Mechanical Layer 1 \*.qml is Board Outline, slots and circular cutouts  
Mechanical Layer 2 \*.qml is footprint notes (not for fab house)  
Mechanical Layer 3 \*.qml is hole location guide  
Mechanical Layer 4 \*.qml is board outline dimensions  
Mechanical Layer 5 \*.qml is topside labels (if no silk)  
Mechanical Layer 6 \*.qml is bottomside labels (if no silk)  
Mechanical Layer 7 \*.qml is FAB drawing notes  
Mechanical Layer 8 and up are not for usage by PCB board house  
\*.gts and \*.gbo are top and bottom layer silkscreen (aka overlay)  
\*.gts and \*.gbs are top and bottom soldermask  
\*.drl is NC Drill  
\*.spr is aperture file  
\*.gpb and \*.gpt are pad master layers, and are not used (ignore if in zip file)  
Keepout layer \*.gko is for internal usage only, and is not to be used by board house

FAB Drawing

1. Finished PCB is RoHS
2. Dimensions are in inches, unless otherwise noted.
3. Applicable Standards:
  - 3a. Manufacture in accordance to IPC-6011, IPC-6012.
  - 3b. UL Approved to a minimum category of 94V0.
4. Laminate:
  - 4a. Thickness: 0.062inch
  - 4b. Type: FR4
5. Copper:
  - 5a. Layer Count: 2
  - 5b. Exterior layers: 4oz min
  - 5c. Interior layers: N/A
  - 5d. Plated through holes: plate to 1mil min copper thickness
  - 5e. Trace separation: 10mil
  - 5f. Trace min width: 10mil
6. Surface Finish:
  - 6a. Immersion Gold
  - 6b. This line left intentionally blank.
7. Soldermask:
  - 7a. Top/Bottom soldermask required
  - 7b. Soldermask color shall be green.
8. Silkscreen:
  - 8a. Top/bottom silkscreen required.
  - 8b. Silkscreen color shall be white.
  - 8c. Min silkscreen line width: 8mil
  - 8d. Epoxy or acrylic ink allowed
9. Drill holes:
  - 9a. No blind or buried vias.
  - 9b. Hole sizes are specified after plating.
10. Mill separate (or V-score) according to mech1 (\*.g1) layer.
11. Contact information:
  - 11a. Shaun Upton, [supton@allegromicro.com](mailto:supton@allegromicro.com), 603.626.2429
  - 11b. If fast turn board, 24hr contact info: N/A



Symbol	Hit Count	Tool Size	Physical Length	Rout Path Length	Plated	Hole Type
□ Feature is to be drilled here	69	15mil (0.381mm)			PTH	Round
○	10	42mil (1.067mm)			PTH	Round
×	2	56mil (1.422mm)			PTH	Round
▽	5	62mil (1.575mm)			PTH	Round
◇	4	125mil (3.175mm)			PTH	Round
⊘	2	266mil (6.756mm)			PTH	Round
⊞	1	50mil (1.27mm)	550mil (13.97mm)	500mil (12.7mm)	NPTH	Slot
113 Total						

Slot definitions : Rout Path Length = Calculated from tool start centre position to tool end centre position.  
Physical Length = Rout Path Length + Tool Size = Slot length as defined in the PCB layout

